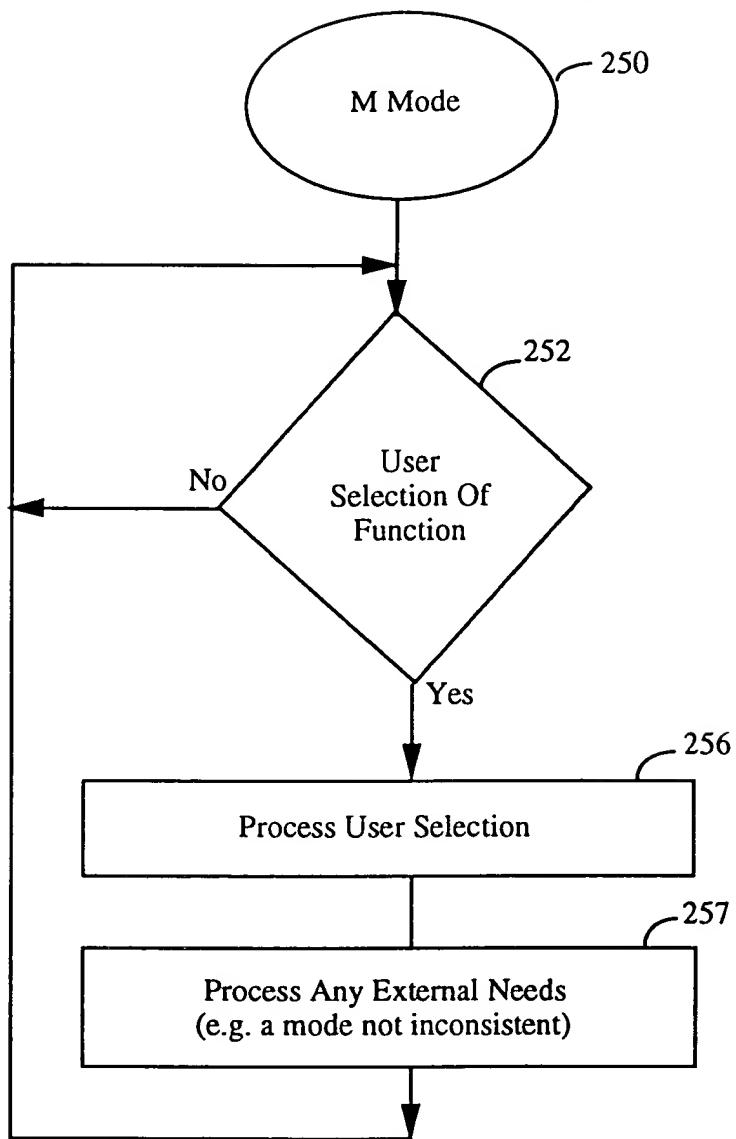


FIG. 1B





*FIG. 2B*

*FIG. 2C*

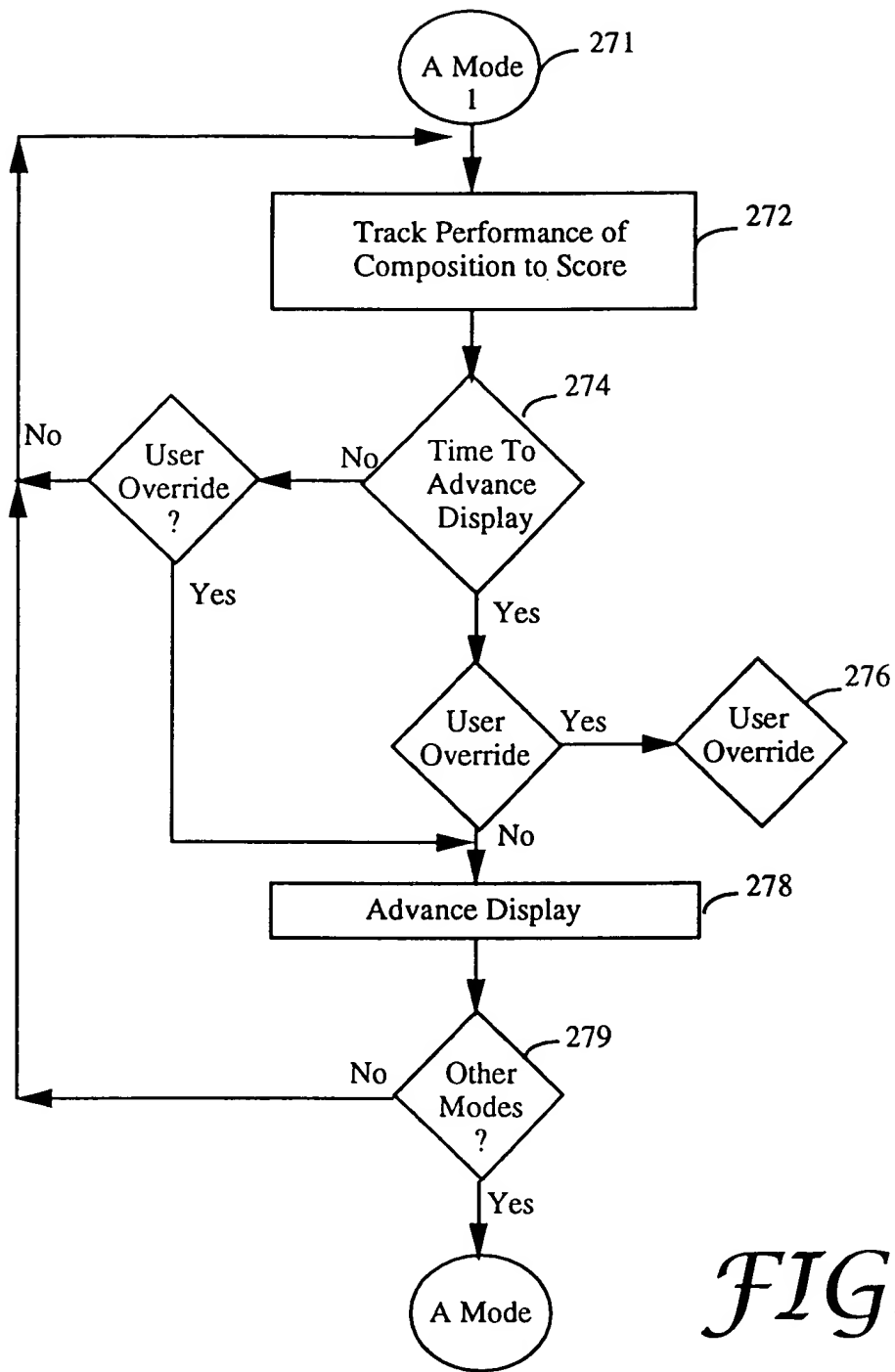
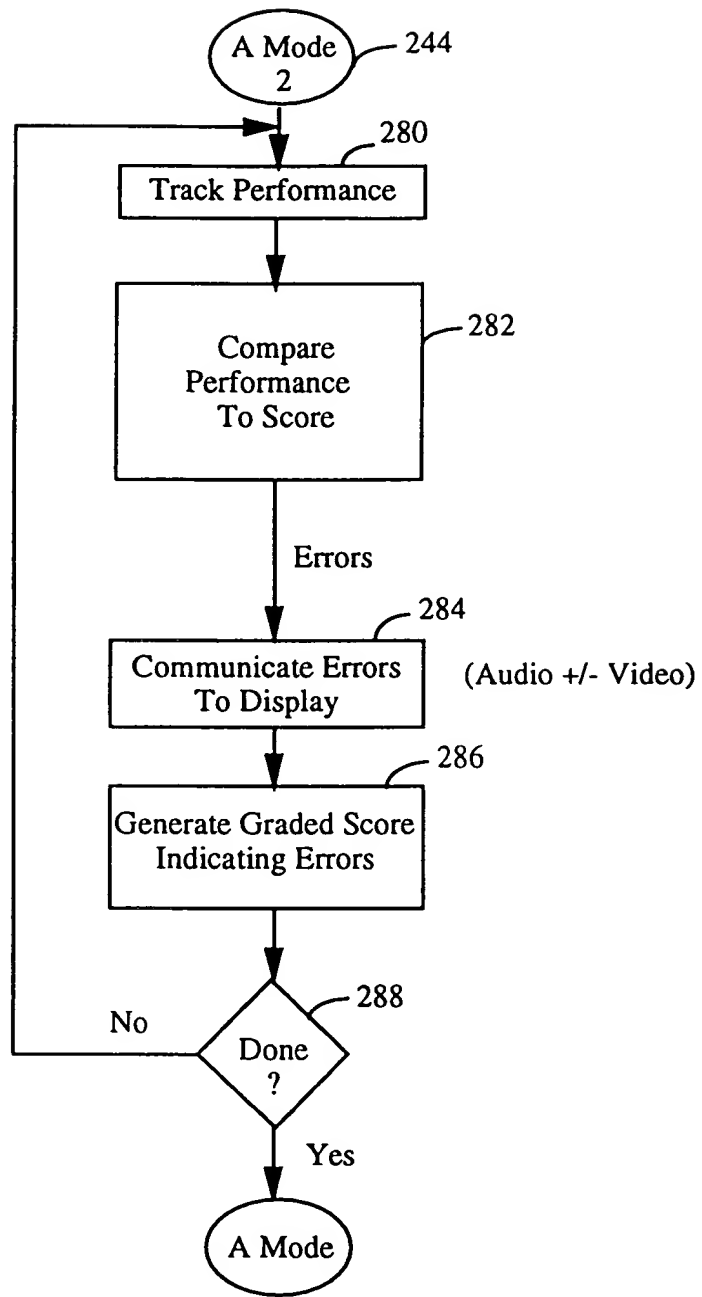
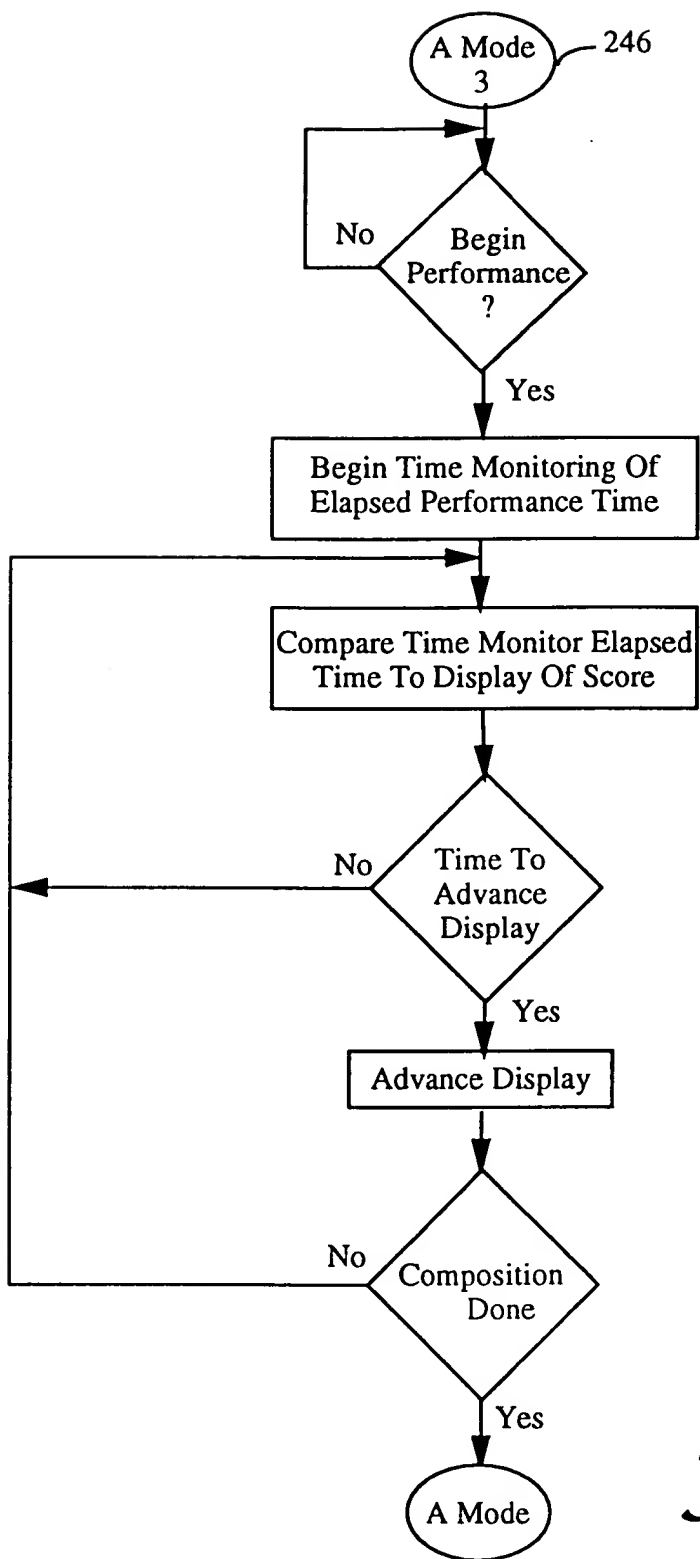


FIG. 2D



*FIG. 2E*

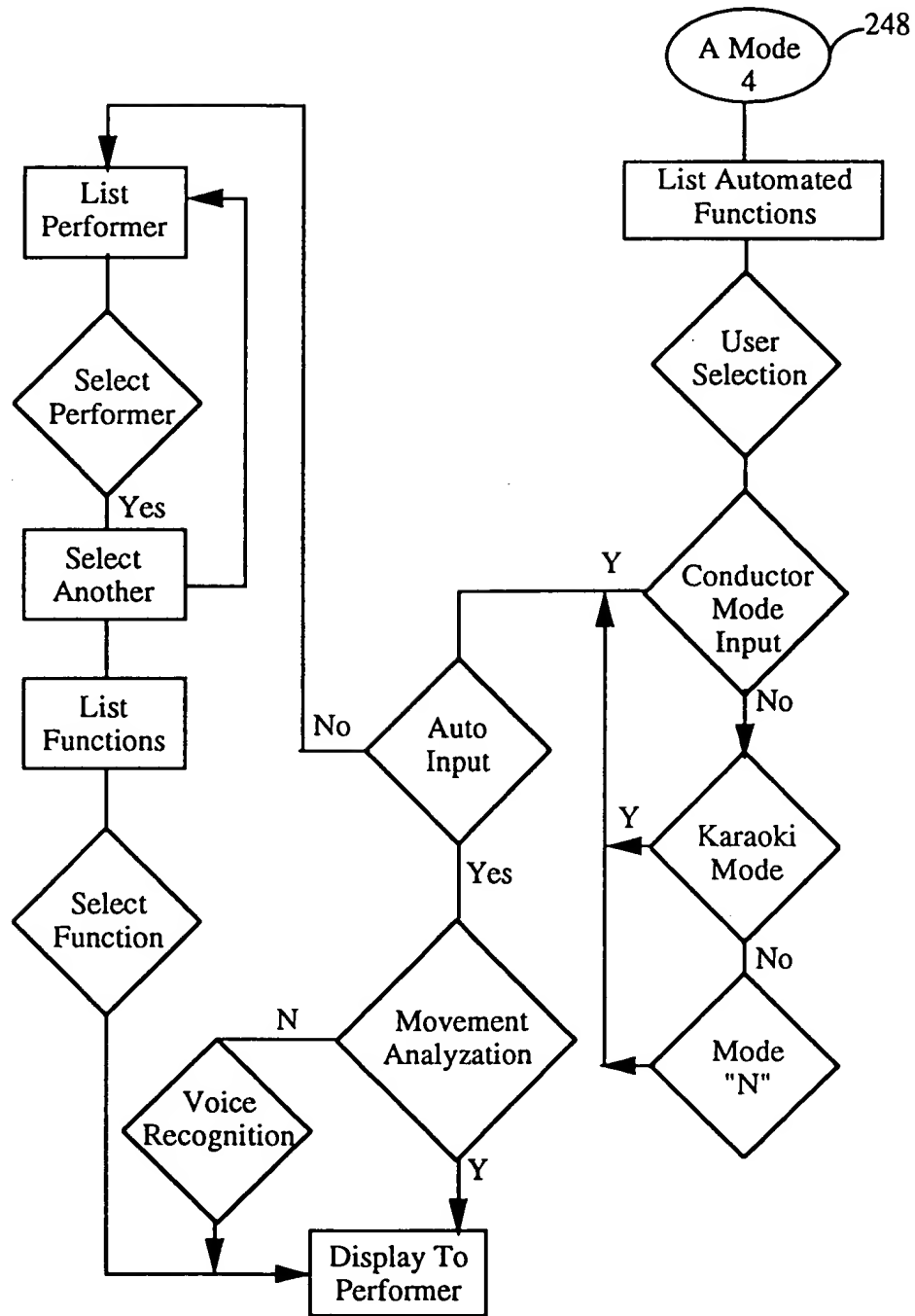


*FIG. 2F*



FIG. 2G

CONDUCTOR MODE



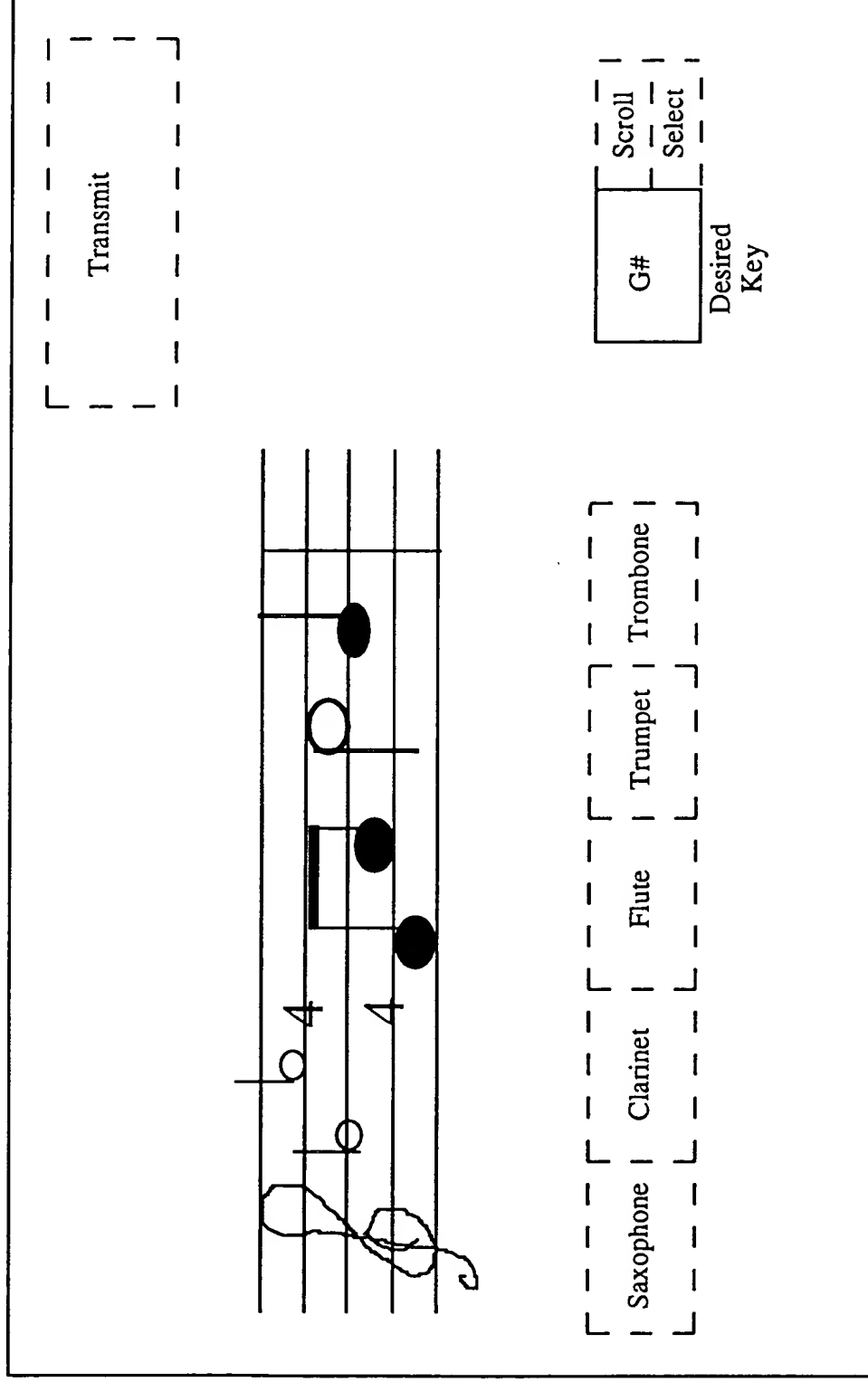


FIG. 3

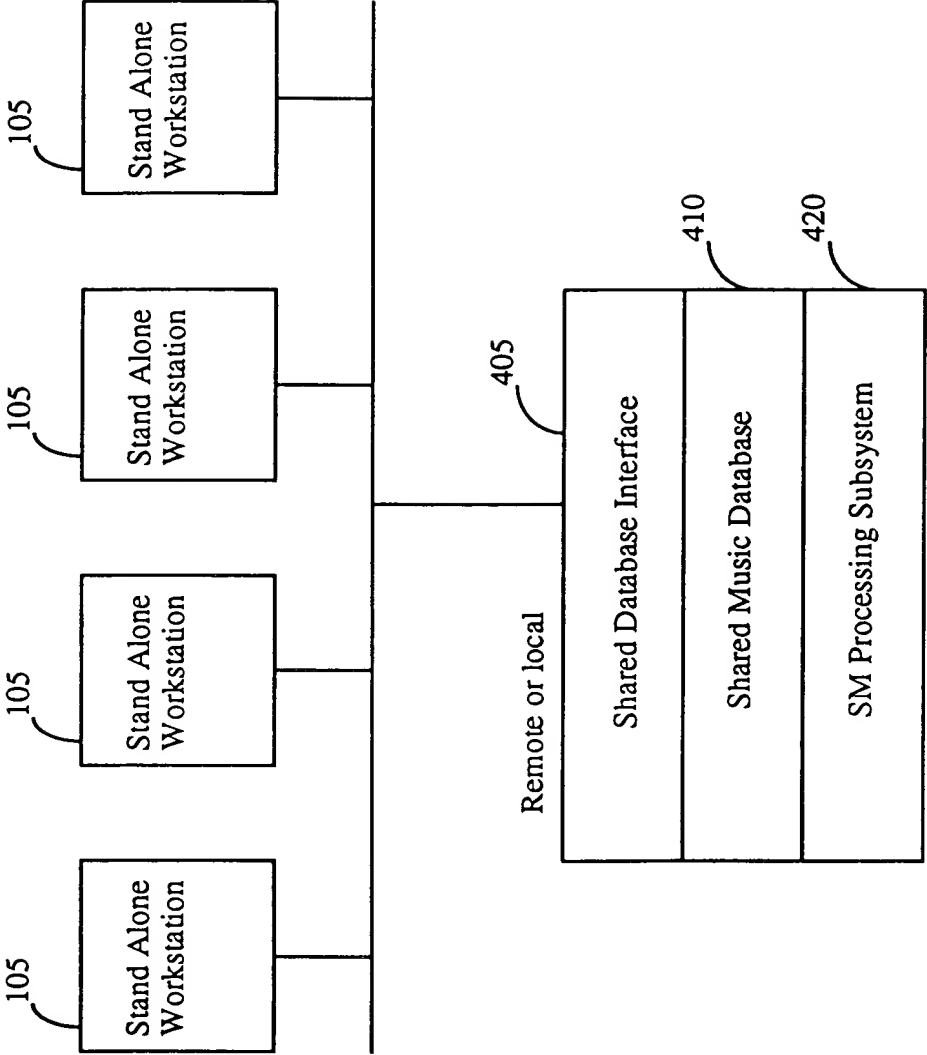


FIG. 4

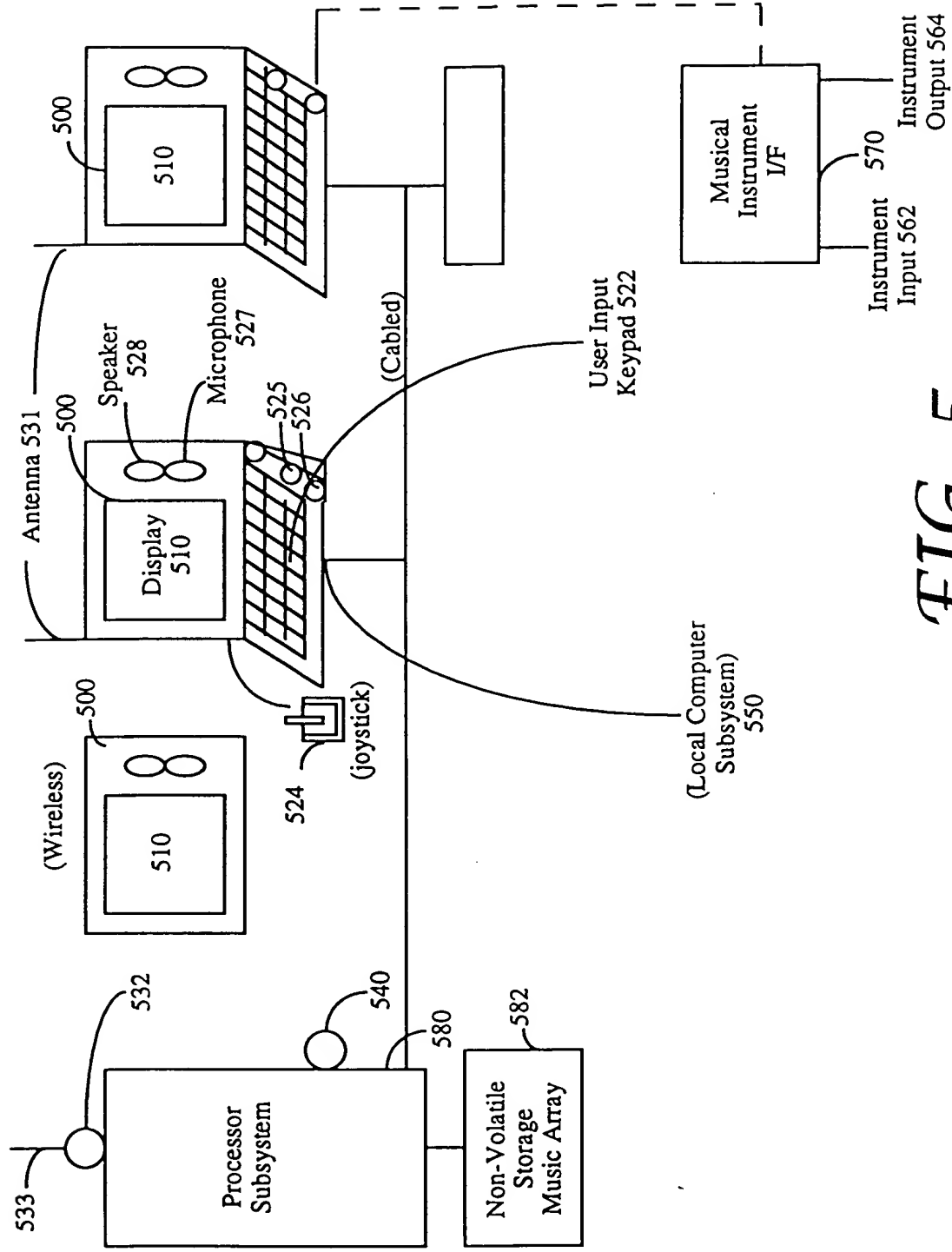


FIG. 5

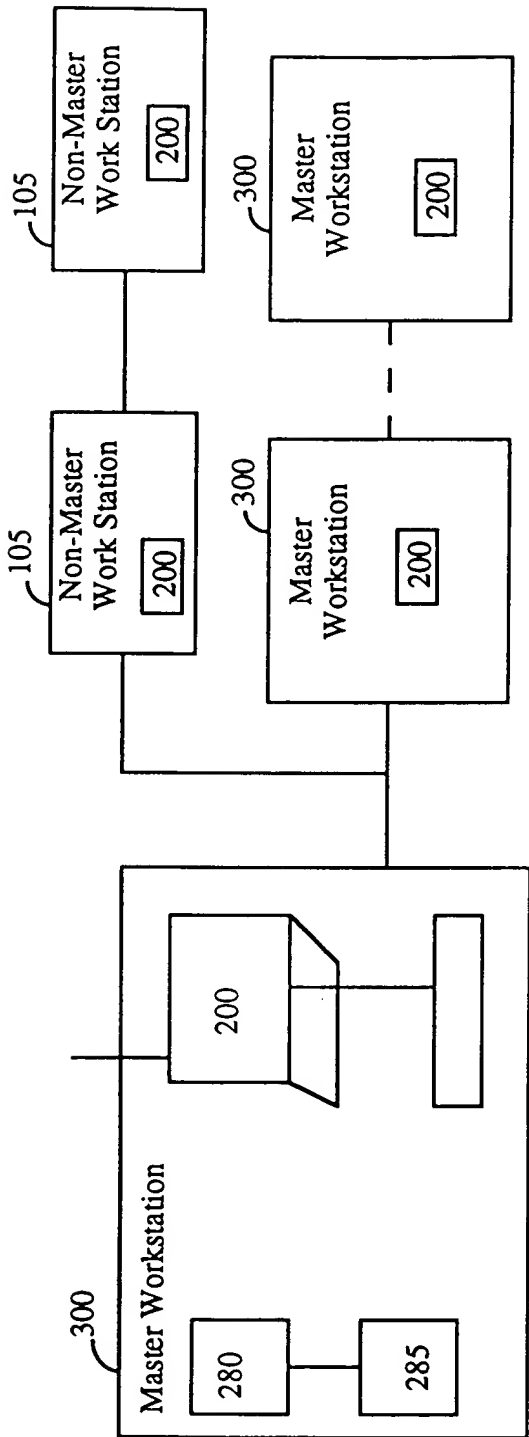


FIG. 6

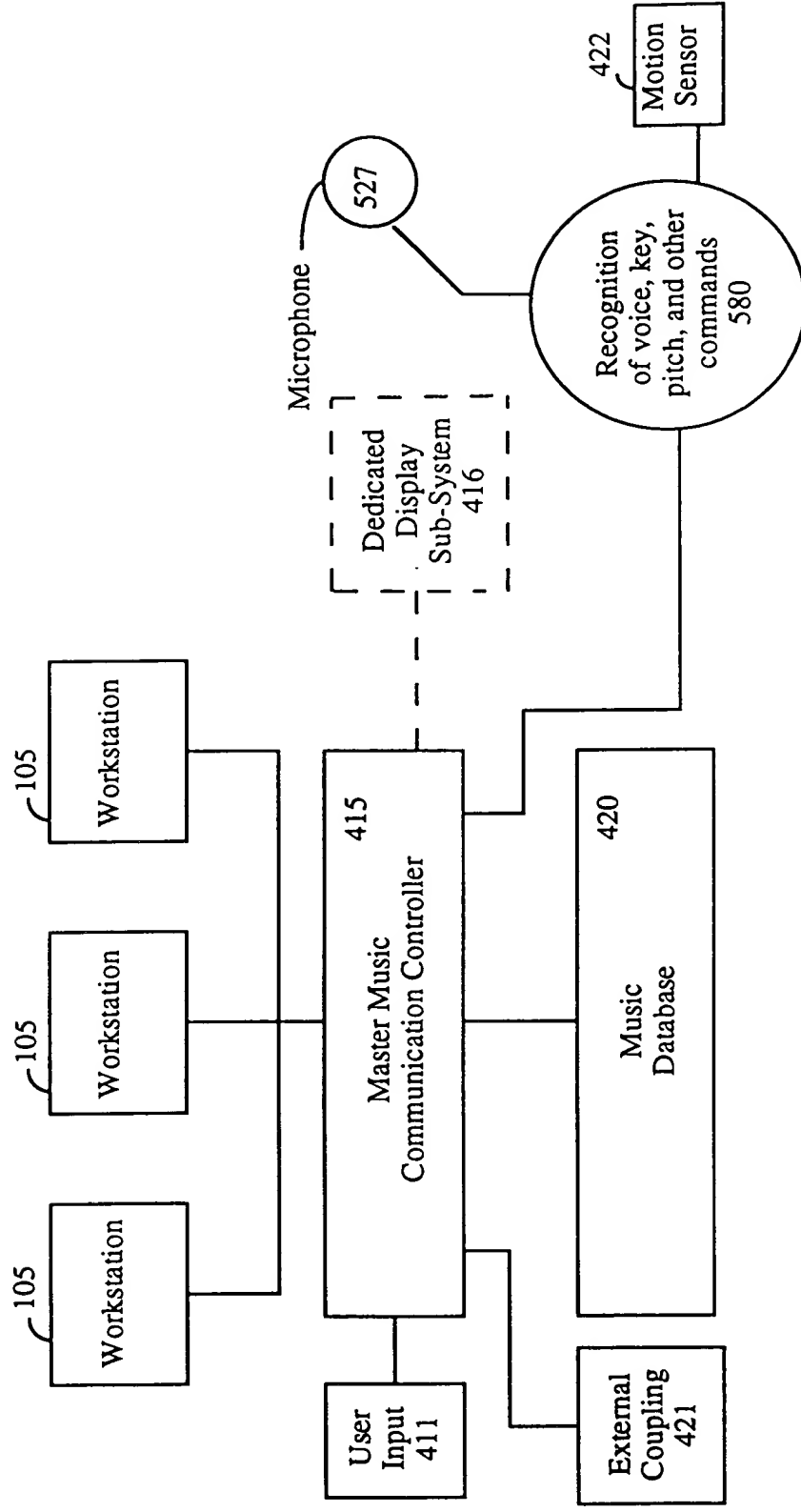
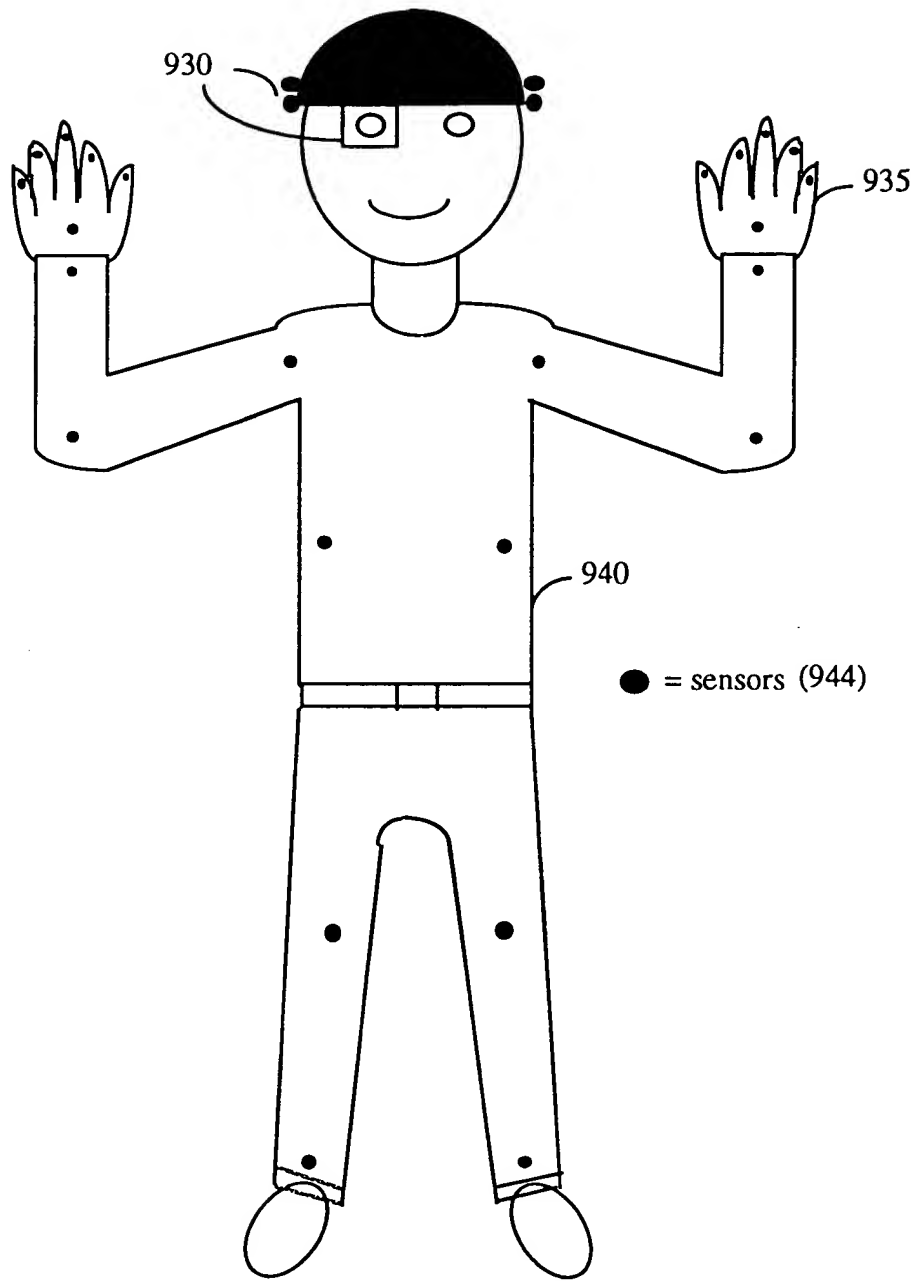


FIG. 7

FIG. 8



*FIG. 9*



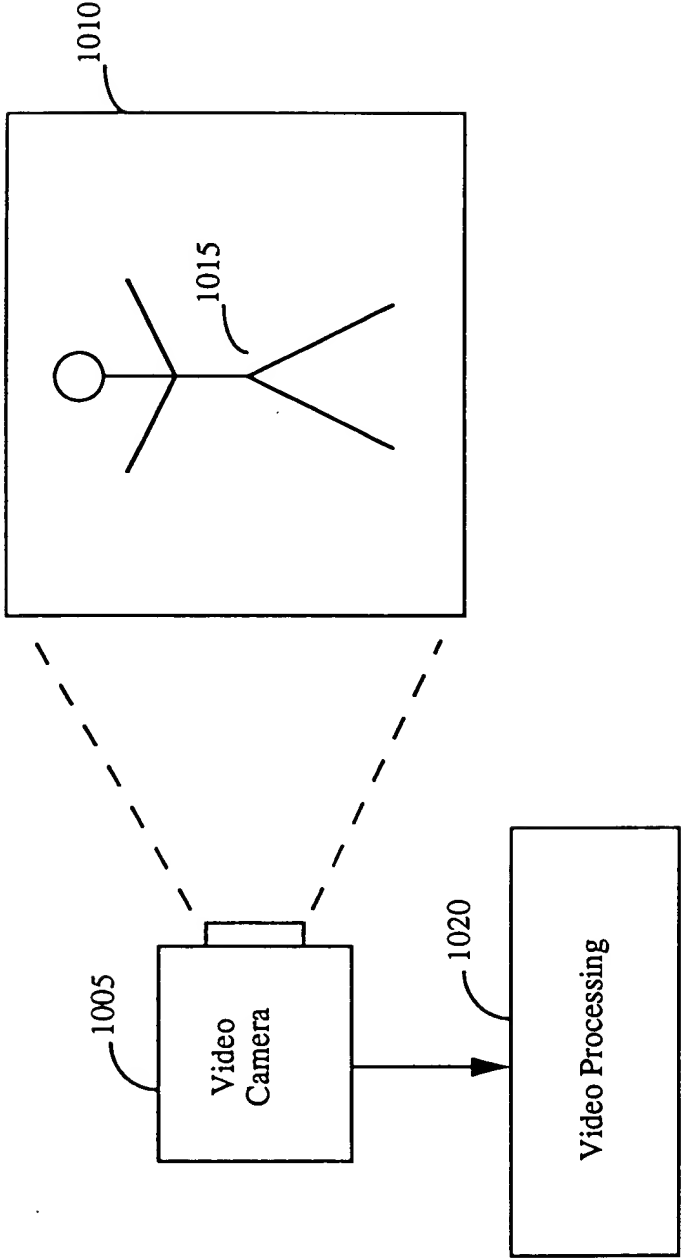


FIG. 10

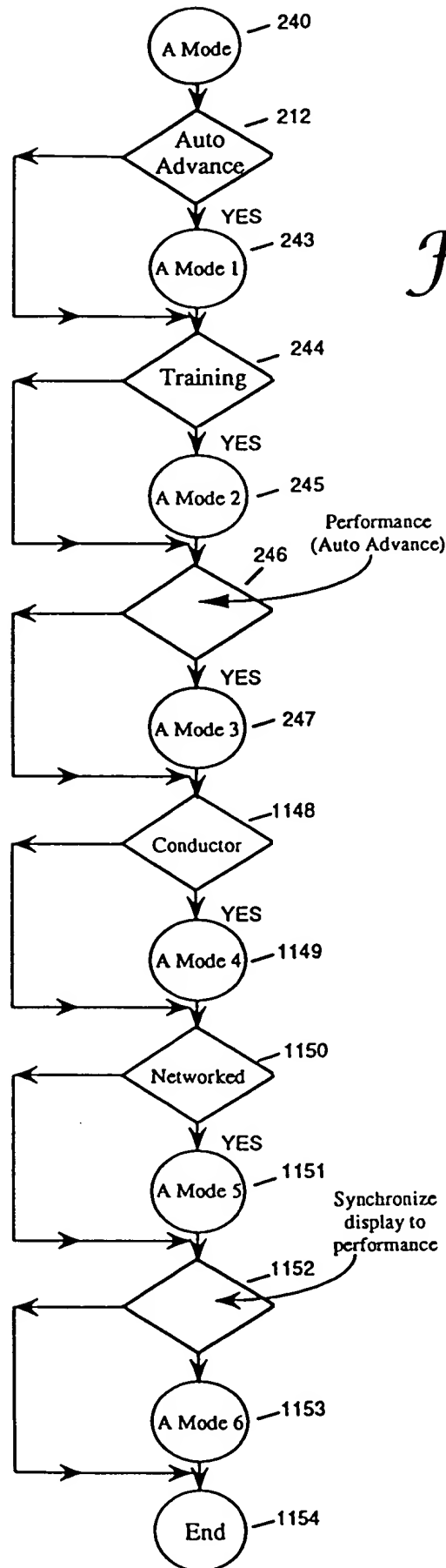
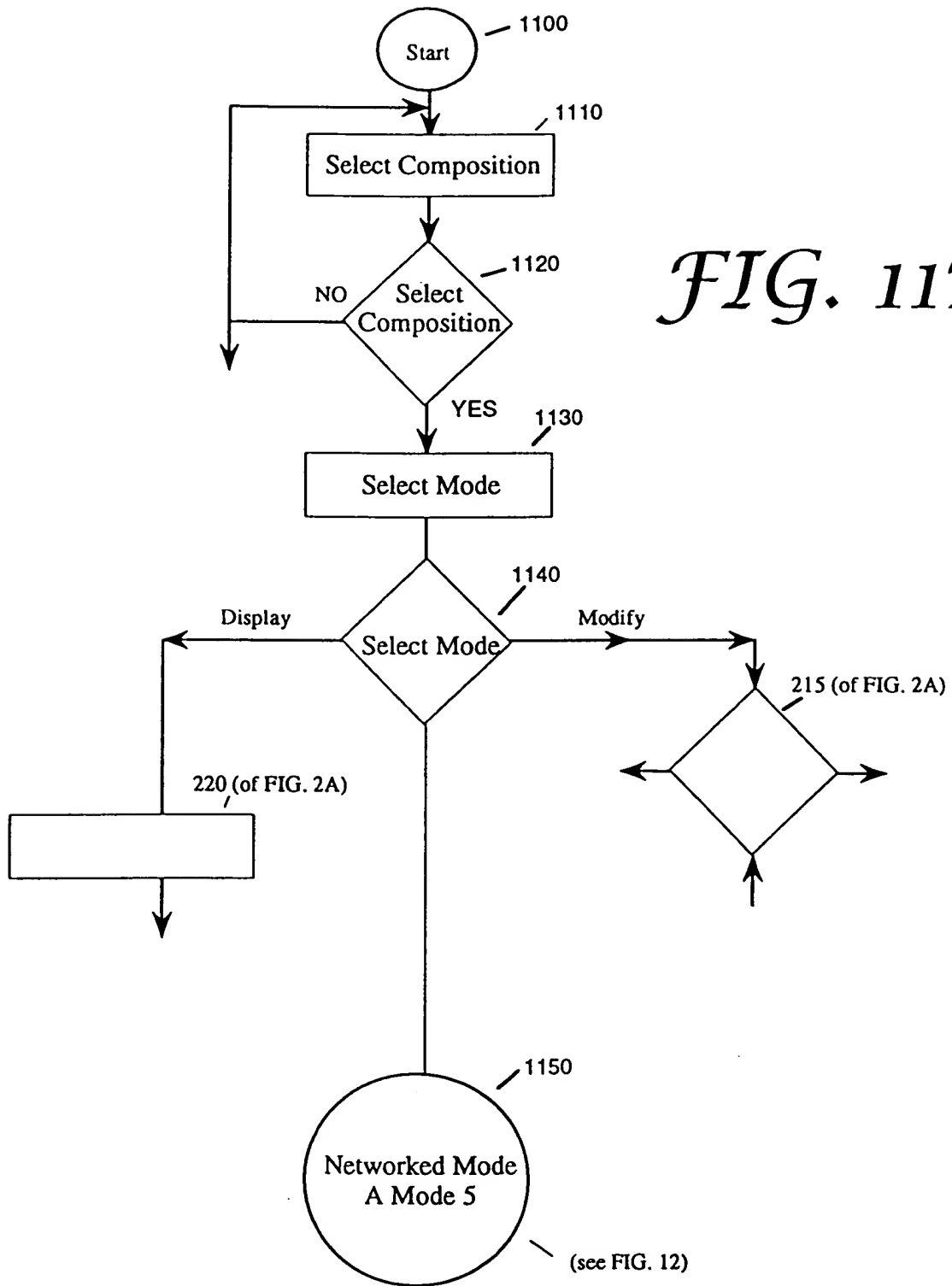


FIG. 11A



*FIG. 11B*

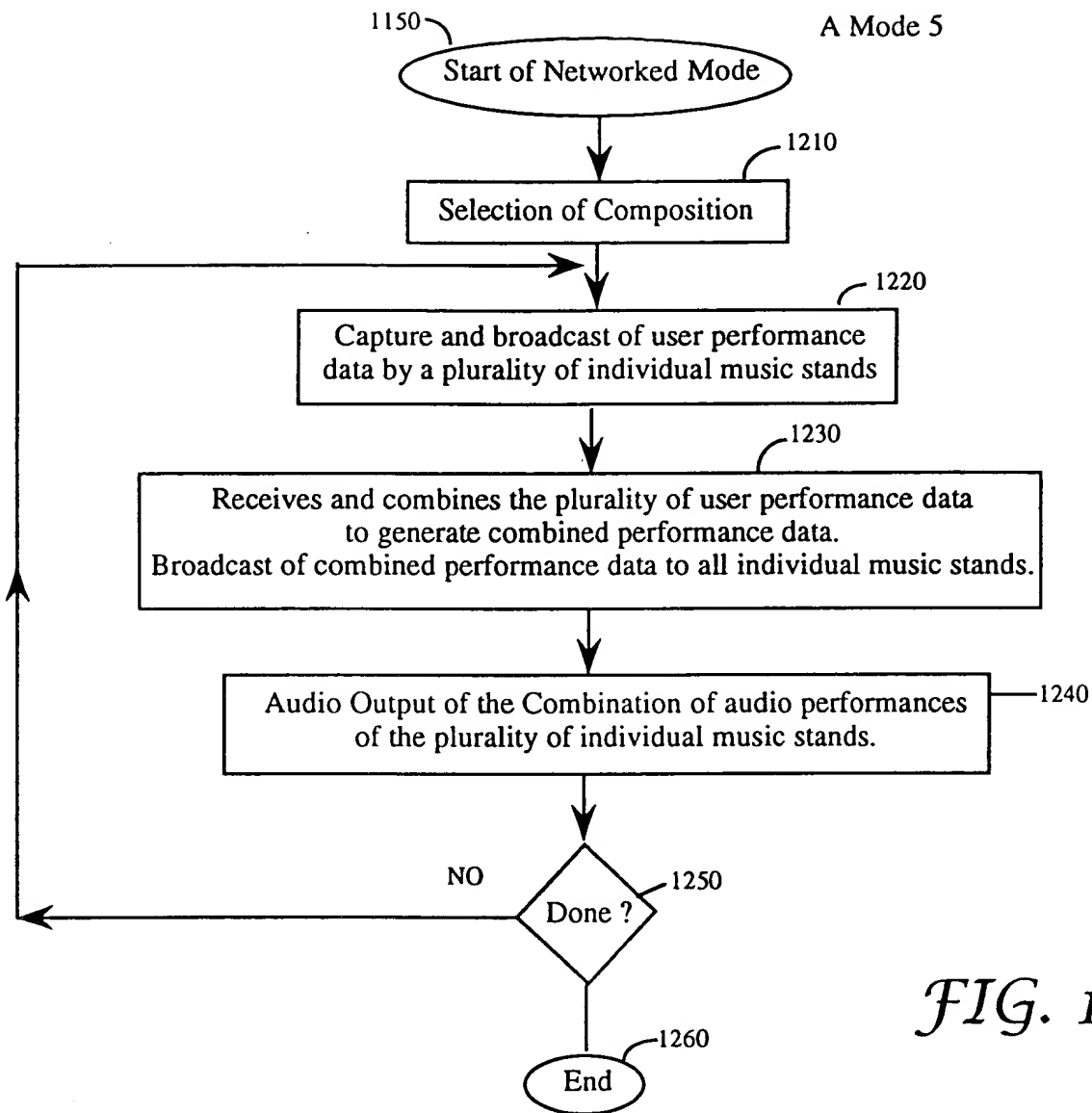
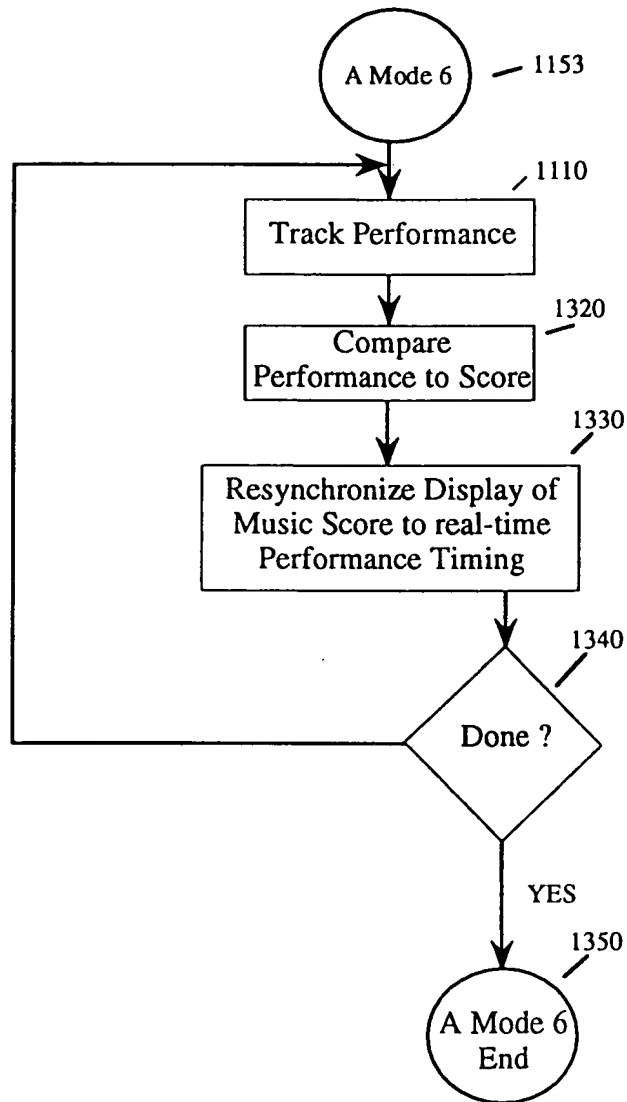


FIG. 12

*FIG. 13*



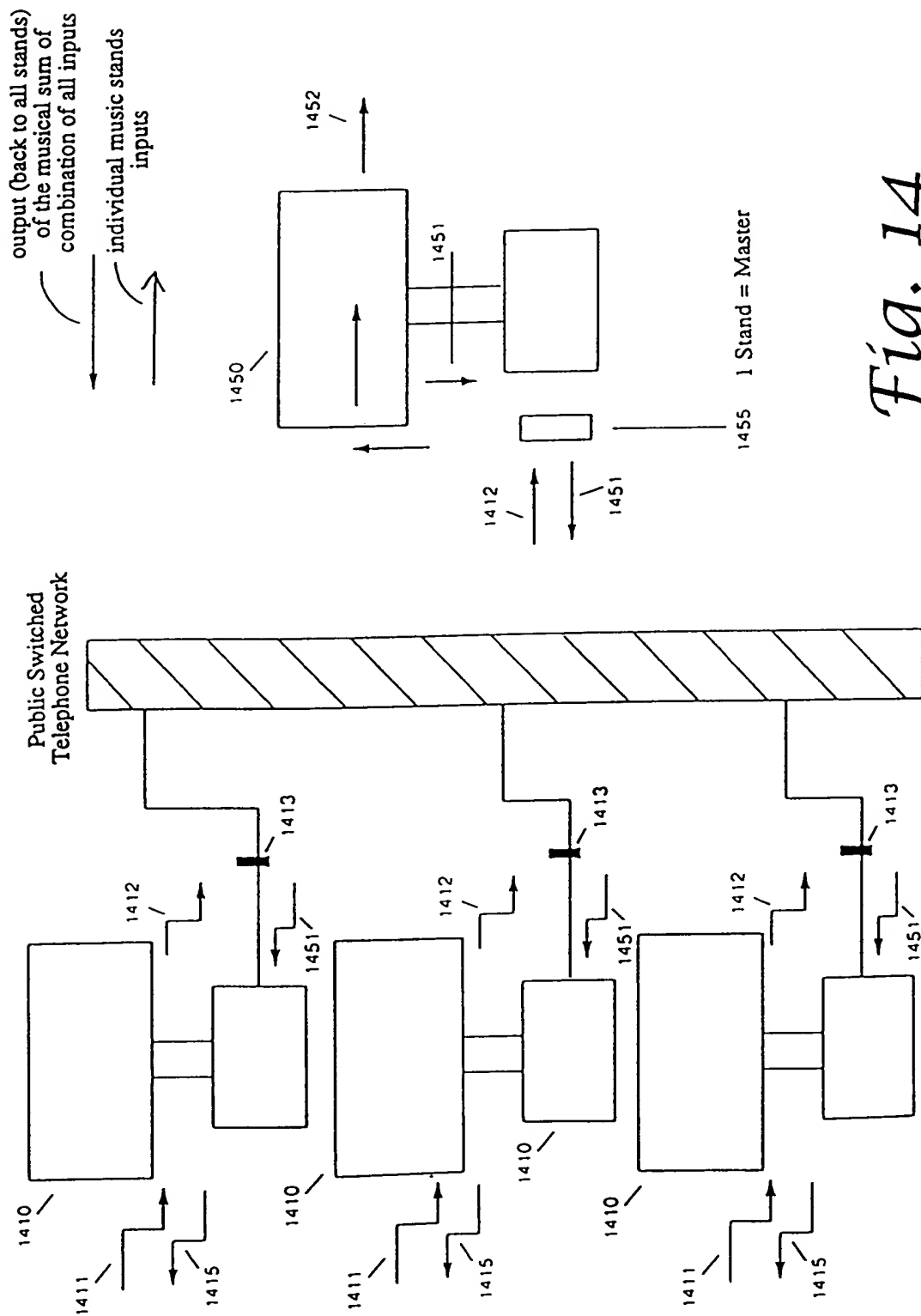


Fig. 14

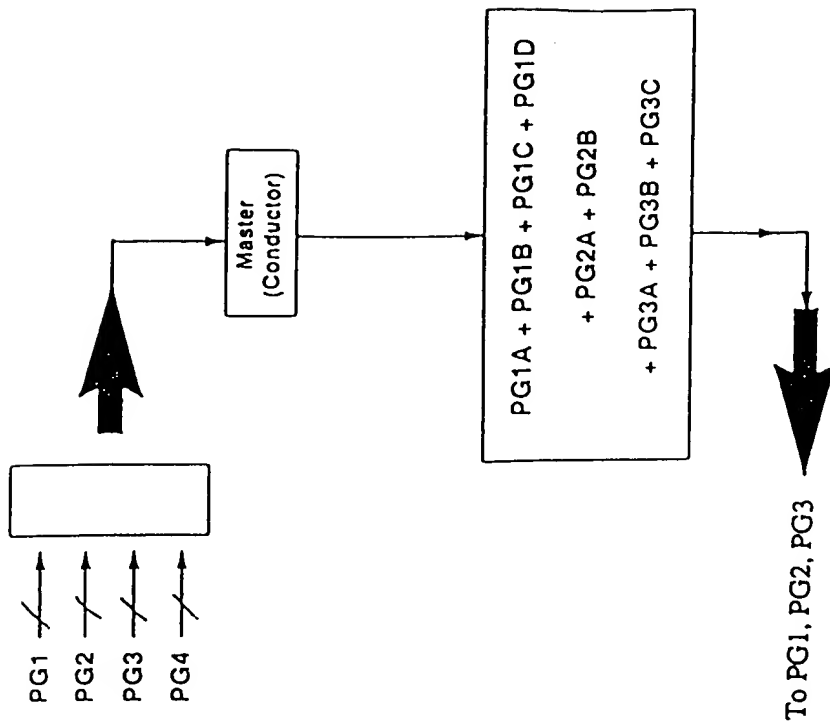
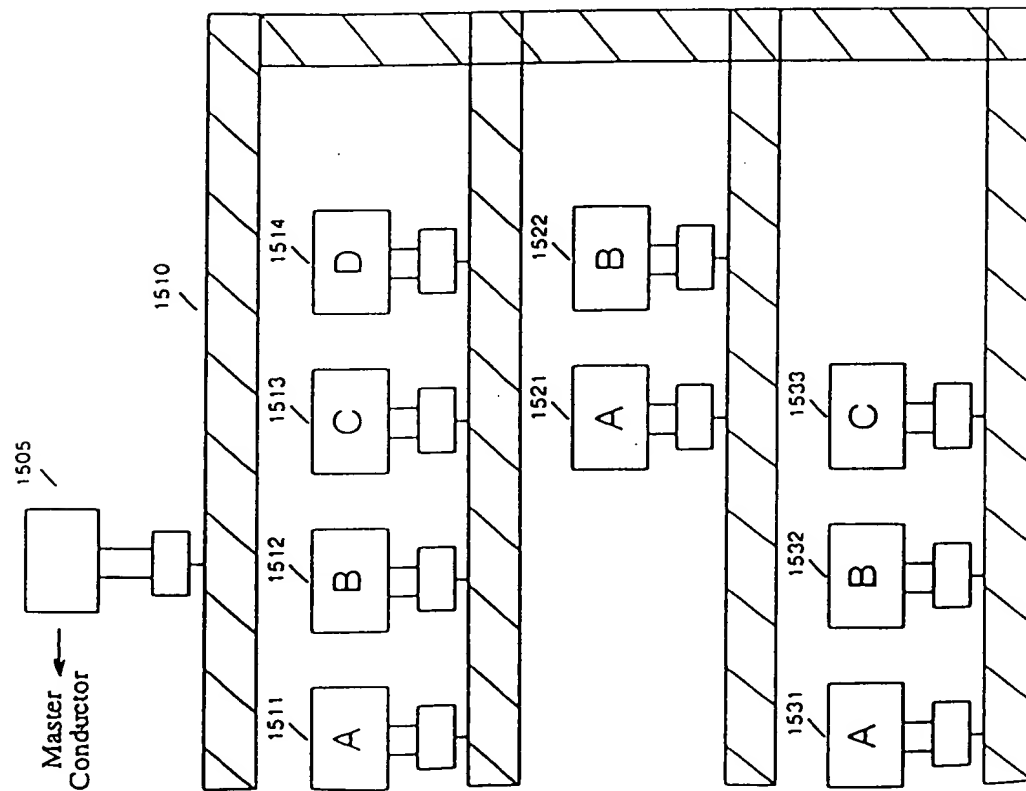
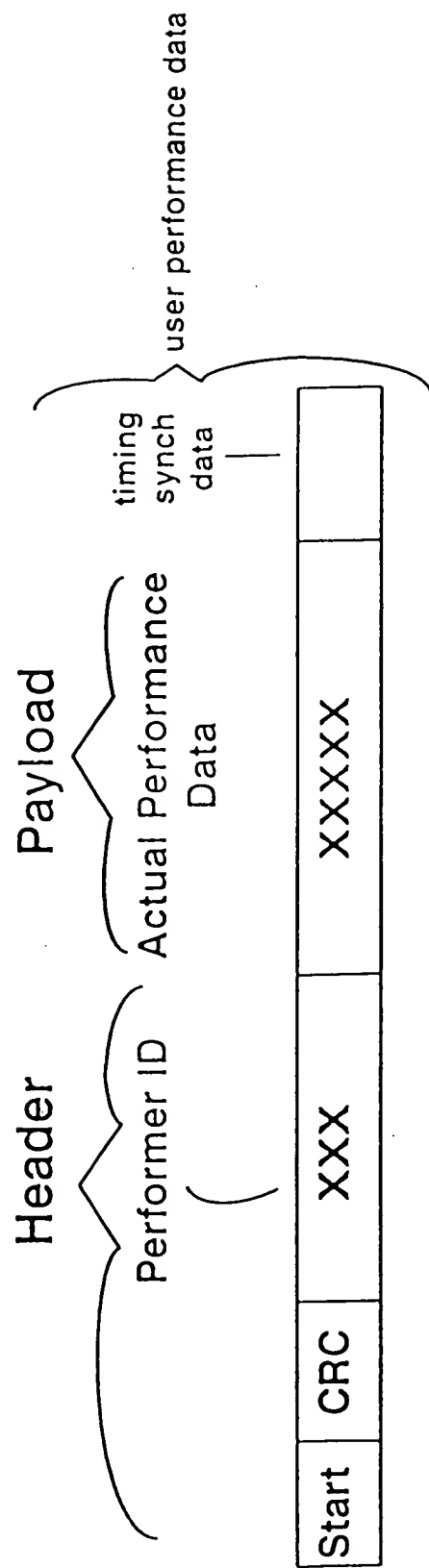


Fig. 15

Fig. 16





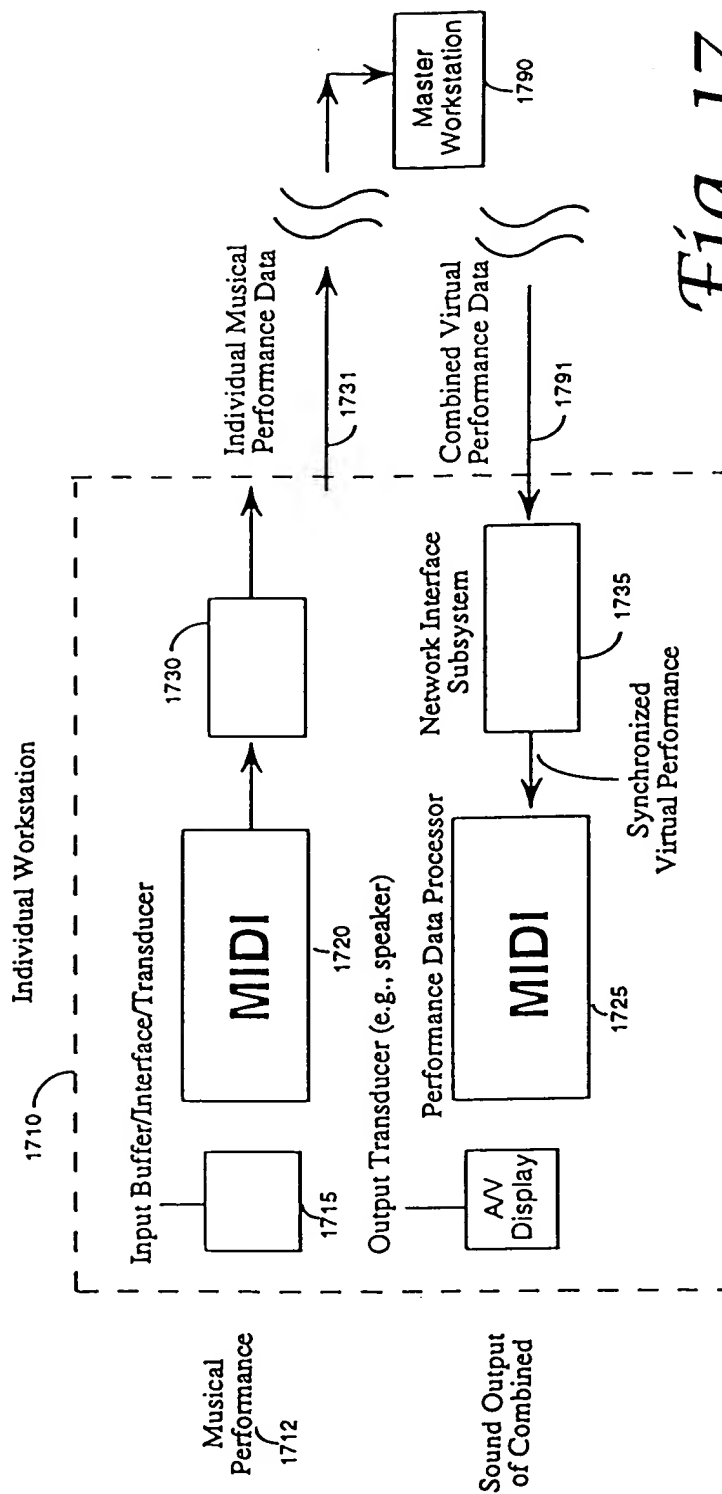
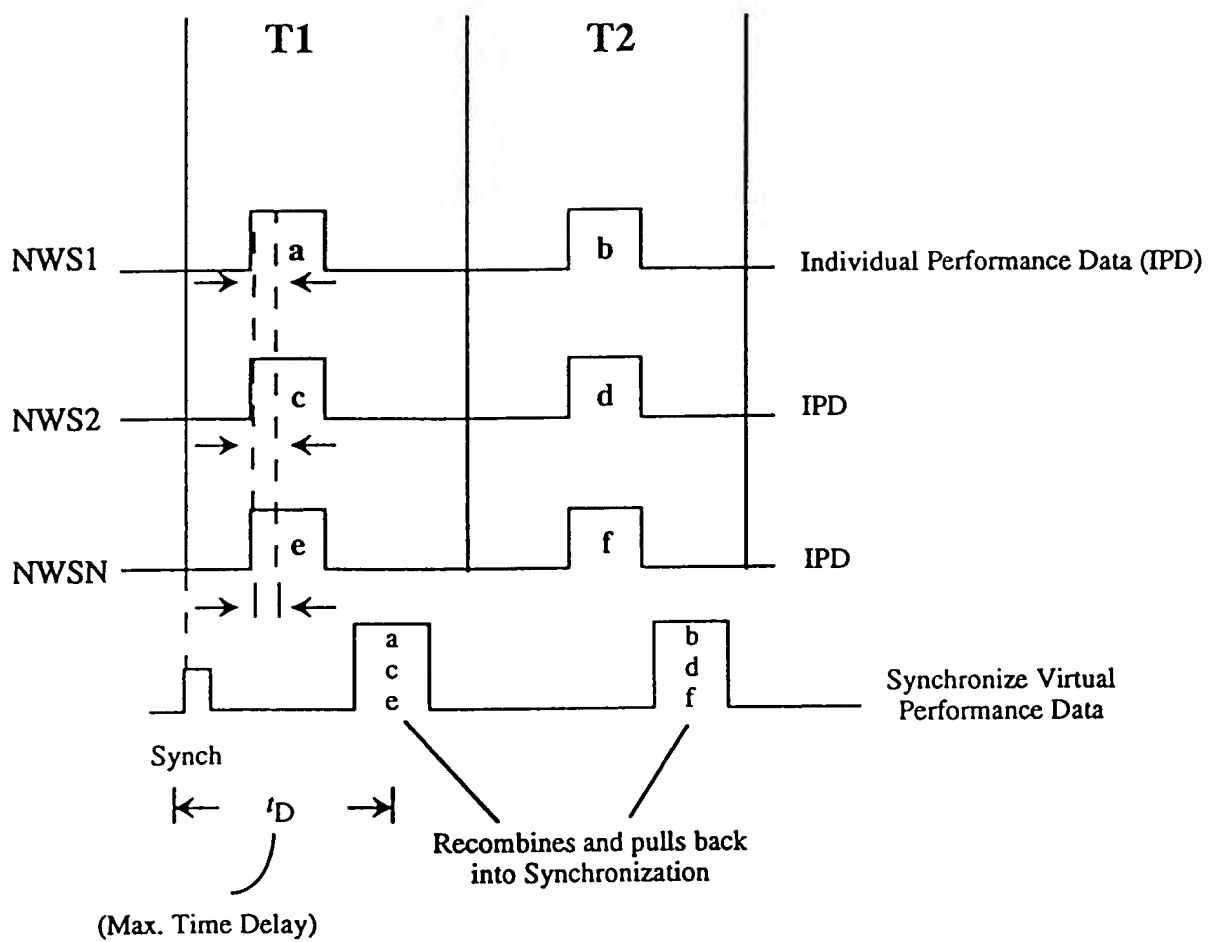


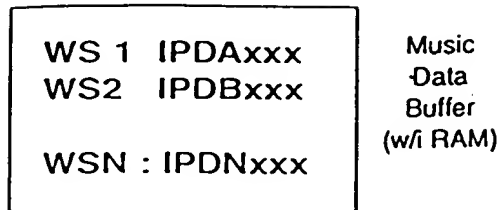
Fig. 17



a, b, c, d, e, f, = data and timing synch info

*Fig. 18*

## Master Station Buffer

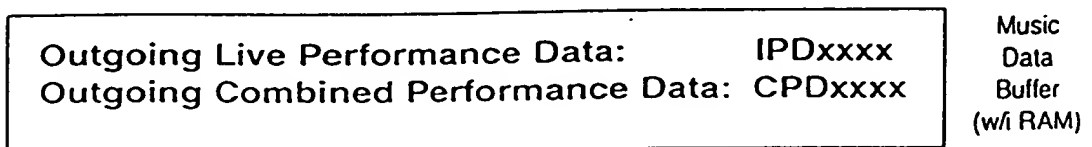


Combined Performance Data:  $Axxx + Bxxx + \dots + Nxxx = CPDXXX$

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## Individual Work Station



*Fig. 19*